

Innovative insulation technology for SiC Power Electronics

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Innovative insulation technology for SiC Power Electronics





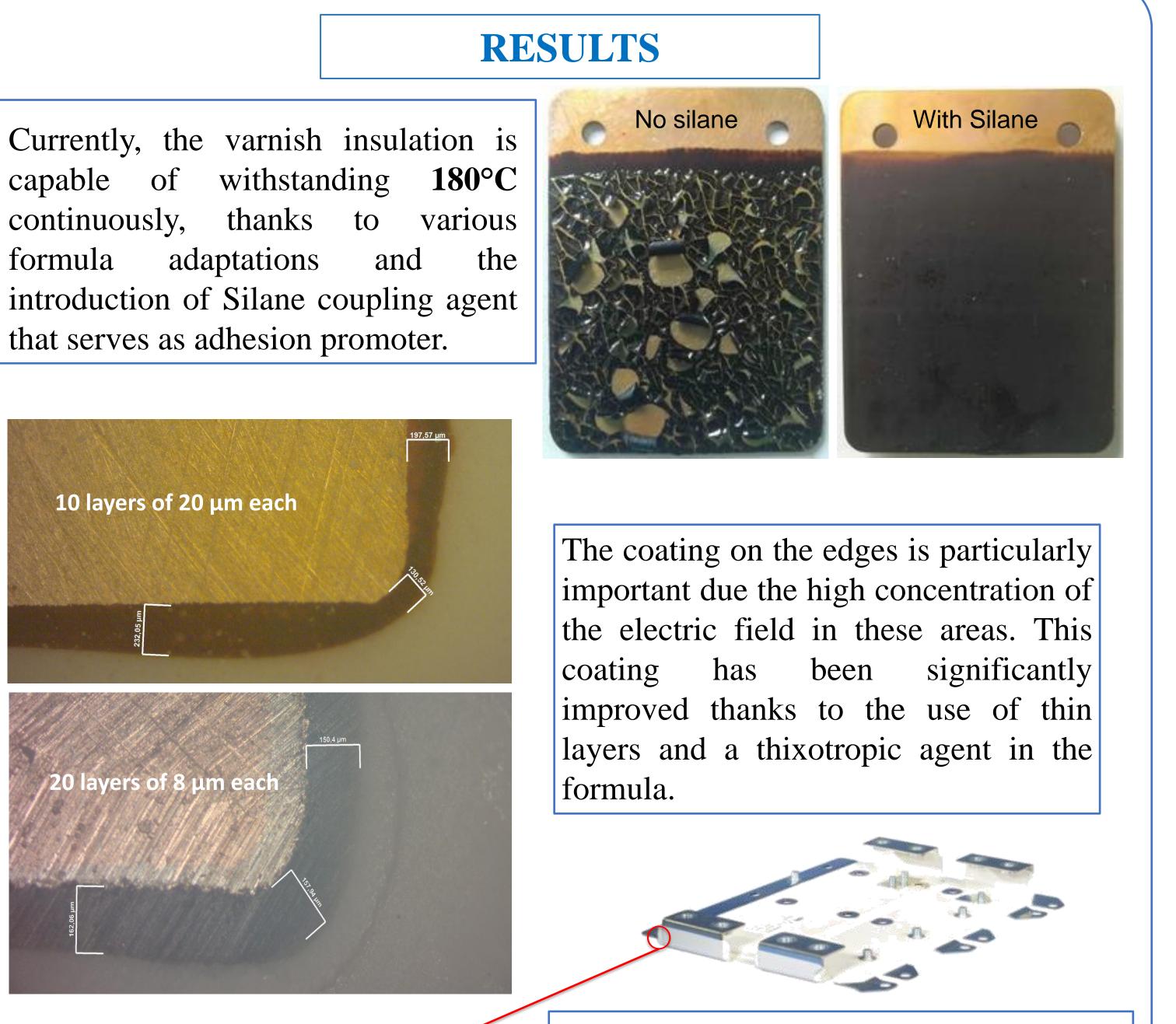
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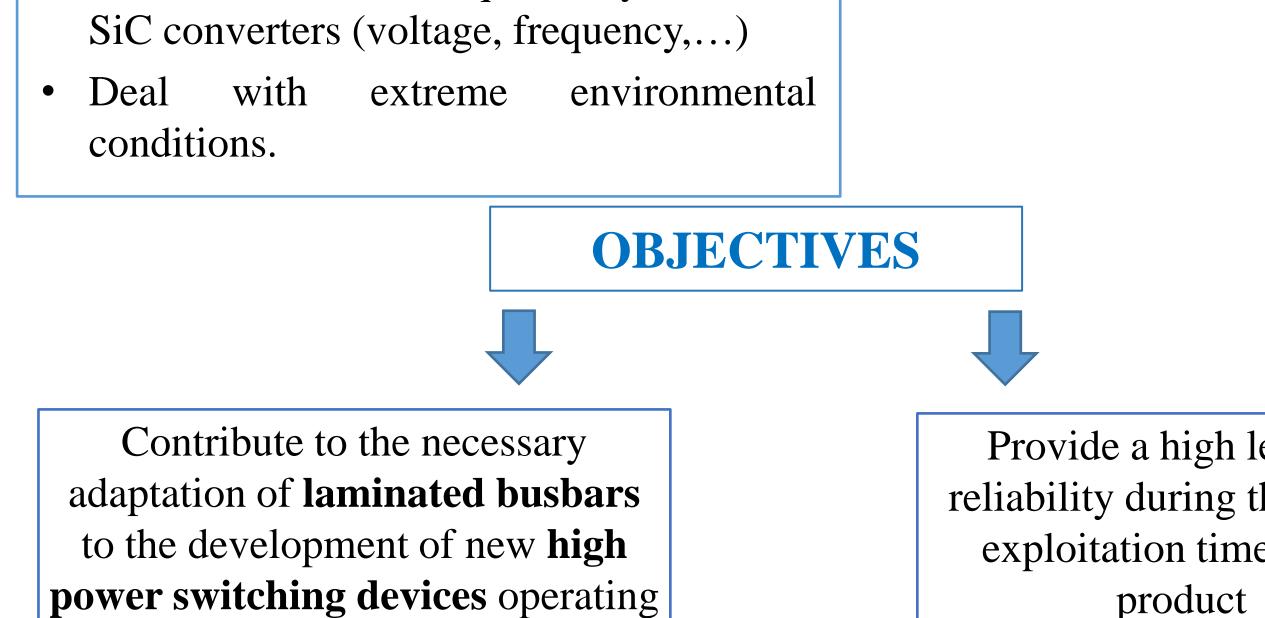
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INTRODUCTION

- A laminated busbar (LBB) manufacturing today is faced with multiple challenges :
- The need to increase service life of the product;
- the partial Limit appearance of discharges;
- Reduce the cost and energy consumption.
- Endure constraints required by the futur







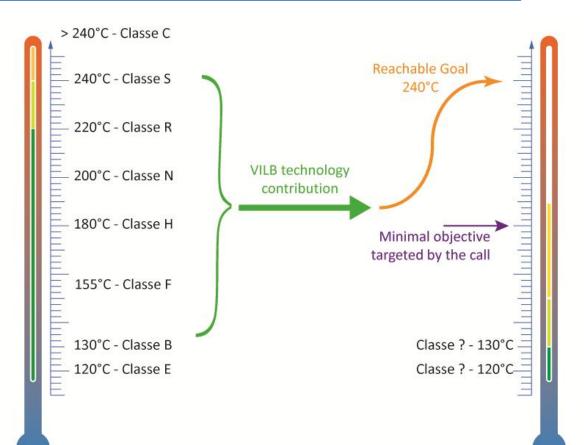
Provide a high level of reliability during the whole exploitation time of the product

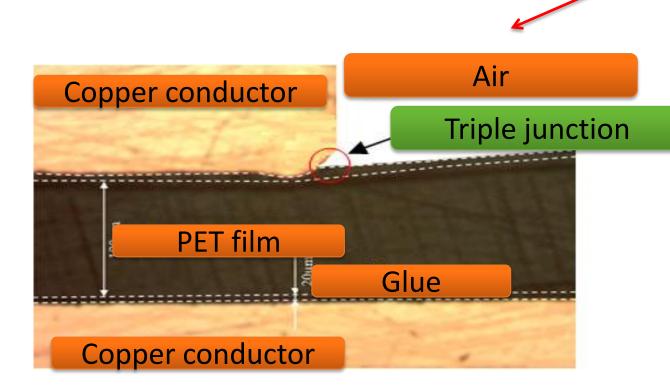
VARNISH INSULATED BUSBAR (VIB)

VIB is used for the first time in This busbars. laminated new technology is based on existing varnish insulation in electrical machines.

in extreme conditions

The process consists of depositing multiple very thin layers (~ 10 microns) of varnish using a micro spray gun and curing with UV light after each layer.



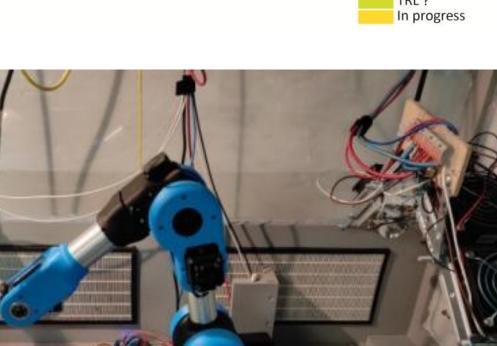


The breakdown voltage tests performed on the dielectric are rather promising : •9.5 kV AC for 100µm on the surface. •2.5 kV AC for100 µm on the edges.

The insulation the critical areas of the **busbar** needs further improvement

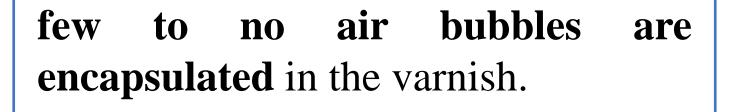
Solution ? fill the triple junction with a dielectric that contains as little air cavities as possible.





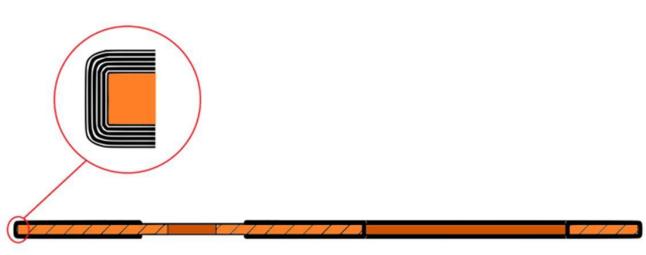
Busbar Industry

TRL 9 since many years



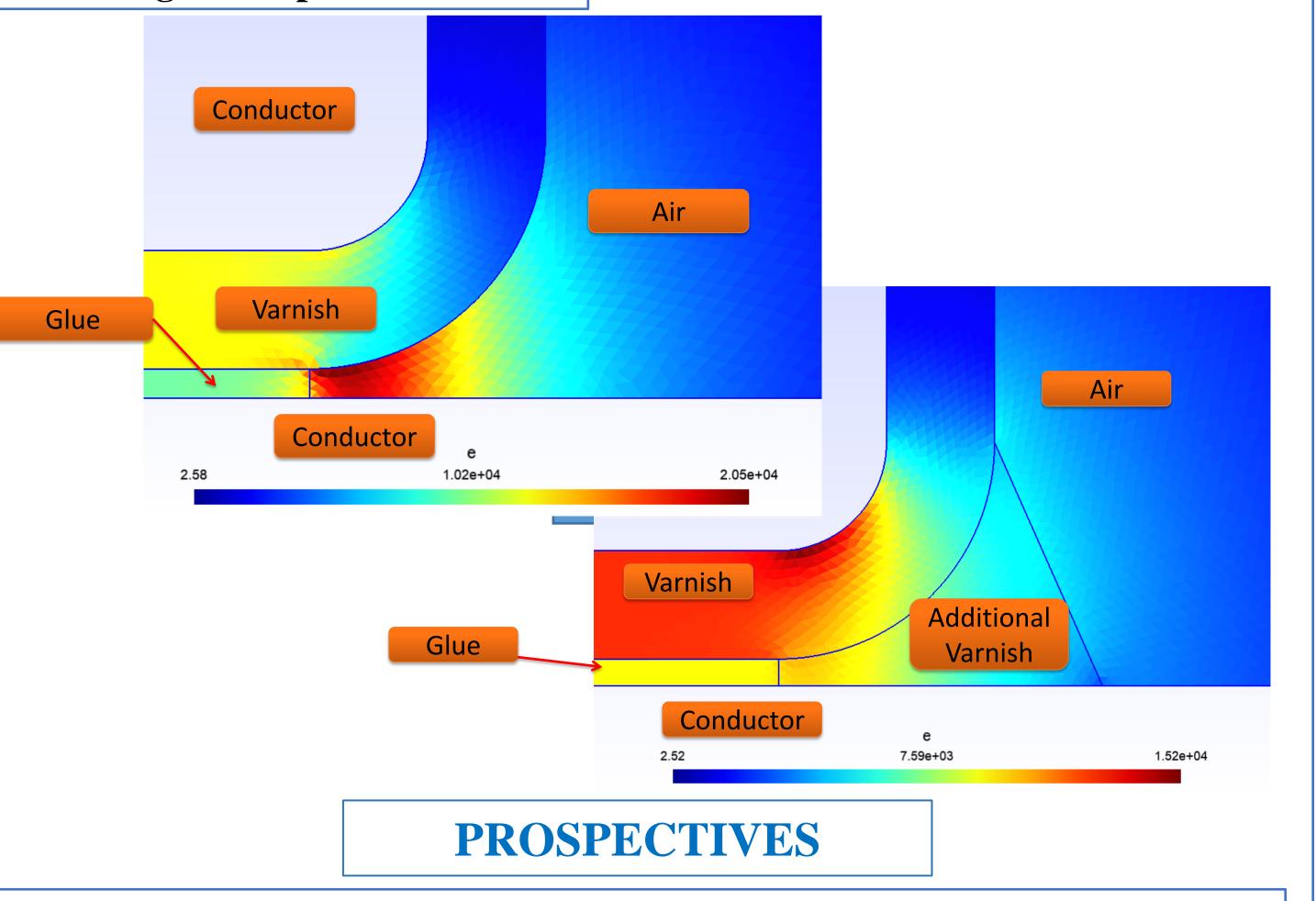


high dielectric strength and high PDIV.

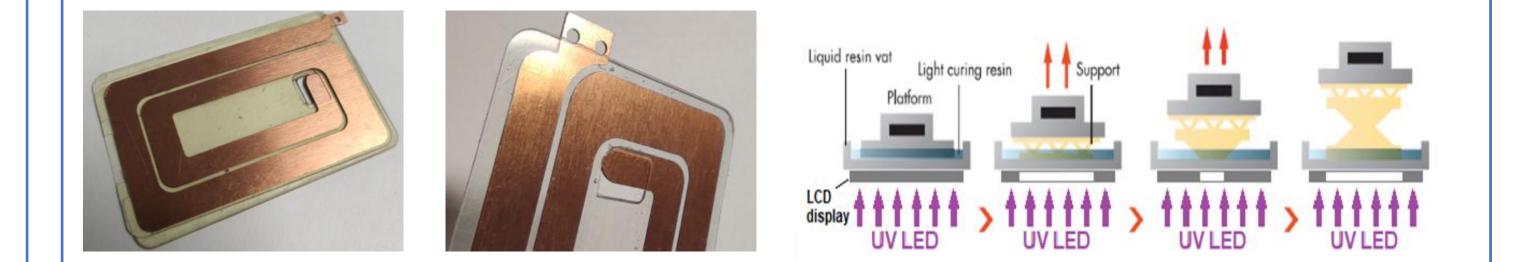


VIB concept - Cross section of a conductor

much lower electric field intensity in the air in these areas. higher PDIV and a longer life span.



• Introduction of micro & nano inorganic fillers (BaSO4, SiO2, Al2O3,



An alternative manufacturing process (patent application filed) is to use additive manufacturing, which consists of 3D printing successive layers using SLA – LCD technology on each face of the conductor. Well suitable for flat busbar (example : conductors for power planar transformer):

- complete encapsulation, including lateral sides (creepage distance, compacity - bubble-free (partial discharge)

TiO2) to improve performances in : thermal resistance, creepage discharge performance, corona resistance ...

- Use of an **electret layer** (varnish with BaTiO3 ferroic filler) in to significantly • limit the electric field intensity.
- Combine UV curing with thermal curing using a thermal curing agent. ullet
- Characterization of a laminated busbar performance in a simulated \bullet aeronautical environment.

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